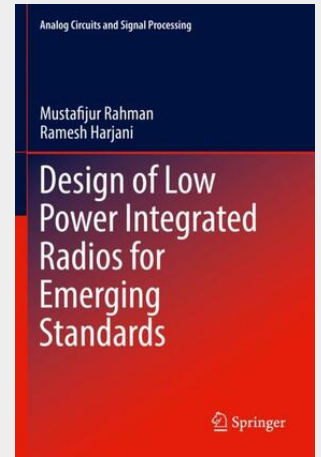


## Design of Low Power Integrated Radios for Emerging Standards

This book describes novel and disruptive architecture and circuit design techniques, toward the realization of low-power, standard-compliant radio architectures and silicon implementation of the circuits required for a variety of leading-edge applications. Readers will gain an understanding of the circuit level challenges that exist for low power radios, compatible with the IEEE 802.15.6 standard. The authors discuss current techniques to address some of these challenges, helping readers to understand the state-of-the-art, and to address the various, open research problems that exist with respect to realizing low power radios. - Enables readers to face challenging bottleneck in low power radio design, with state-of-the-art, circuit-level design techniques; - Provides readers with basic knowledge of circuits suitable for low power radio circuits compatible with the IEEE 802.15.6 standard; - Discusses new and emerging architectures and circuit techniques, enabling applications such as body area networks and internet of things.



**90,94 €**

84,99 € (zzgl. MwSt.)

Lieferfrist: bis zu 10 Tage

**Artikelnummer:** 9783030213350

**Medium:** Buch

**ISBN:** 978-3-030-21335-0

**Verlag:** Springer International Publishing

**Erscheinungstermin:** 14.08.2020

**Sprache(n):** Englisch

**Auflage:** 1. Auflage 2020

**Serie:** Analog Circuits and Signal Processing

**Produktform:** Kartoniert

**Gewicht:** 154 g

**Seiten:** 71

**Format (B x H):** 155 x 235 mm

